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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No	6,713,346 B2
Patent Issue Date	
Application Serial No	
Filing Date	
Assignee	Micron Technology, Inc.
Inventorship	Graham Wolstenholme
Attorney's Docket No	MI55-003
Title: Methods of Forming a Line of FLASH Memory	

Title: Methods of Forming a Line of FLASH Memory Cells

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR APPLICANT MISTAKE and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)

To:

Commissioner for Patents

Certificate

P.O. Box 1450

Alexandria, VA 22313-1450

AUG 1 8 2004

ATTN:

Decision and Certificate of Correction

Branch of the Patent Issue Division

of Correction

From:

Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,713,346 B2, granted March 30, 2004, in accordance with the Certificate of Correction form attached hereto in duplicate.

It is noted that an error appears in this patent of a typographical nature of character, as more fully described below. The error occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

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The exact page and line number where the error occurs in the application file is:

Page 6, line 18 in the specification; Col. 3, line 31 in the issued patent;

Page 33, line 3 in the specification; Abstract, line 2 in the issued patent.

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.

Enclosed is a check in the amount of \$100.00, as required by 37 CFR 1.20(a).

Respectfully submitted,

Dated: 8-12-0

Ву:

Mark S. Matkin Reg. No. 32,268

16 2004 PE		U.S. Pater	PT0/S8/21 (02-04 Approved for use through 07/31/2006. OMB 0651-003 nt and Trademark Office: U.S. DEPARTMENT OF COMMERC		
Under the Paperwork	Reduction Act of 1995, no person		on of information unless it displays a valid OMB control numbe		
- OFMARIE - TO A A L			09/768,878		
	SMITTAL	Filing Date	January 23, 2001		
	ORM	First Named Inventor	Graham Wolstenholme		
(to be used for all corres	spondence after initial filing)	Art Unit	2812		
		Examiner Name	Richard A. Booth		
Total Number of Pages in	Total Number of Pages in This Submission 9 Attorney Docket Number MI55-003				
	ENC	LOSURES (Check all tha	t apply)		
Amendment/Rep After Fina Affidavits Extension of Time Express Abandor Information Disclet Certified Copy of Document(s)	ched 143724 Indicate the description of the declaration (s) Indicate the	uer No. 021567	to Technology Center (TC) Appeal Communication to Board of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please Identify below): PTO Return Receipt Postcard Req for Cert of Correction Cert of Correction in duplicate		
	cation Issued I e to Missing Parts CFR 1.52 or 1.53	tent No. 6,713,346 B2 March 30, 2004			
	SIGNATURE C	OF APPLICANT, ATTORN	EY, OR AGENT		
Firm or Mark S. Matkin, Reg. No. 32,268 Wells St. John P.S.					
Signature Signature					
Date	Date 8/12/04				
CERTIFICATE OF TRANSMISSION/MAILING					
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. Typed or printed name					
	Muriel G. Dunnigan	. 0	Dota		
Signature	Murul &	1. Aungan) Date August 12, 20024		
This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to					

process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/17 (10-03)
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FEE TRANSMITTAL for FY 2004

Effective 10/01/2003. Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT

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Complete if Known				
Application Number	09/768,878			
Filing Date	January 23, 2001			
First Named Inventor	Graham Wolstenholme			
Examiner Name	Richard A. Booth			
Art Unit	2812			
Attenney Docket No.	MT55_003			

METHOD OF PAYMENT (check all that apply)	FEE CALCULATION (continued)			
X Check Credit card Money Other None				
X Deposit Account:	Large Entity Small Entity			
Deposit Account.	Fee Fee Fee Fee Fee Description Code (\$) Code (\$)	ee Paid_		
Account 23-0925	1051 130 2051 65 Surcharge - late filing fee or oath			
Number Deposit	1052 50 2052 25 Surcharge - late provisional filing fee or			
Account Name Wells St. John P.S.	cover sheet 1053 130 1053 130 Non-English specification			
The Director is authorized to: (check all that apply)	1812 2,520 1812 2,520 For filing a request for <i>ex parte</i> reexamination			
Charge fee(s) indicated below X Credit any overpayments	1804 920* 1804 920* Requesting publication of SIR prior to			
X Charge any additional fee(s) or any underpayment of fee(s)	Examiner action			
Charge fee(s) indicated below, except for the filing fee	1805 1,840* 1805 1,840* Requesting publication of SIR after Examiner action			
to the above-identified deposit account.	1251 110 2251 55 Extension for reply within first month			
FEE CALCULATION	1252 420 2252 210 Extension for reply within second month			
1. BASIC FILING FEE Large Entity Small Entity	1253 950 2253 475 Extension for reply within third month	——		
Fee Fee Fee Fee Description Fee Paid	1254 1,480 2254 740 Extension for reply within fourth month			
Code (\$) Code (\$) 1001 770 2001 385 Utility filing fee	1255 2,010 2255 1,005 Extension for reply within fifth month			
1002 340 2002 170 Design filing fee	1401 330 2401 165 Notice of Appeal			
1003 530 2003 265 Plant filing fee	1402 330 2402 165 Filing a brief in support of an appeal			
1004 770 2004 385 Reissue filing fee	1403 290 2403 145 Request for oral hearing			
1005 160 2005 80 Provisional filing fee	1451 1,510 1451 1,510 Petition to institute a public use proceeding			
SUBTOTAL (1) (\$) - 0 -	1452 110 2452 55 Petition to revive - unavoidable			
	1453 1,330 2453 665 Petition to revive - unintentional			
2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE	, , , , , , , , , , , , , , , , , , , ,			
Extra Claims below Fee Paid	71			
Total Claims 20" = X = X	1503 640 2503 320 Plant issue fee			
Claims Multiple Dependent	1460 130 1460 130 Petitions to the Commissioner			
	1807 50 1807 50 Processing fee under 37 CFR 1.17(q)			
Large Entity Small Entity	1806 180 1806 180 Submission of Information Disclosure Stmt Recording each patent assignment per			
Code (\$) Code (\$)	8021 40 8021 40 Recording each patent assignment per property (times number of properties)			
1202 18 2202 9 Claims in excess of 20	1809 770 2809 385 Filing a submission after final rejection			
1201 86 2201 43 Independent claims in excess of 3	(37 CFR 1.129(a)) 1 1810 770 2810 385 For each additional invention to be			
1203 290 2203 145 Multiple dependent claim, if not paid	1 1810 770 2810 385 For each additional invention to be examined (37 CFR 1.129(b))			
1204 86 2204 43 ** Reissue independent claims over original patent	1801 770 2801 385 Request for Continued Examination (RCE)			
1205 18 2205 9 ** Reissue claims in excess of 20 and over original patent	1802 900 1802 900 Request for expedited examination of a design application	100 00		
SURTOTAL (2) (\$) - 0 -:-	Other fee (specify) Req for Cert of Correction 100.0			
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or number previously paid, it greater, i or itelegate, and above				

SUBMITTED BY

Name (Print/Type) Mark S. Matkin Registration No. (Altomet/Agent) 32,268

Signature (Complete (if applicable))

Telephone (509) 624-4276

Date \$//2/04

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This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/17 (10-03)

Approved for use through 07/31/2006. OMB 0651-0032

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for FY 2004 Effective 10/01/2003. Patent fees are subject to annual revision.		Complete if Known			
		Application Number	09/768,878		
		Filing Date	January 23, 2001		
		First Named Inventor	Graham Wolstenholme		
		Examiner Name	Richard A. Booth		
Applicant claims small entity status	. See 37 CFR 1.27	Art Unit	2812		
TOTAL AMOUNT OF PAYMENT	(\$) 100-00	Attorney Docket No.	MI55-003		

METHOD OF PAYMENT (check all that apply)	FEE CALCULATION (continued)			
X Check Credit card Money Other None	3. ADDITIONAL FEES			
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X Deposit Account:	ee Fee Fee Fee	Fee Description	r Daid	
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Account Wells St. John P.S.		cover sheet		
Name The Director is authorized to: (check all that apply)		Non-English specification		
Charge fee(s) indicated below X Credit any overpayments		For filing a request for ex parte reexamination		
Charge any additional fee(s) or any underpayment of fee(s)	1804 920 1804 920	Requesting publication of SIR prior to Examiner action		
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1. BASIC FILING FEE		Extension for reply within third month		
Large Entity Small Entity Fee Fee Fee Fee Fee Paid Fee Paid		Extension for reply within fourth month	_]	
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1005 160 2005 80 Provisional filing fee		6 Petition to revive - unavoidable		
SUBTOTAL (1) (\$) - 0 -		Petition to revive - unintentional		
2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE	i i ž	5 Utility issue fee (or reissue)		
Fee from Extra Claims below Fee Paid	i i	Design issue fee		
Total Claims20** = X =) Plant issue fee		
Independent Slaims - 3** = X = =		Petitions to the Commissioner		
Multiple Dependent =	1807 50 1807 50	Processing fee under 37 CFR 1.17(q)		
Large Entity Small Entity	•	Submission of Information Disclosure Stmt		
Fee Fee Fee Fee Fee Description Code (\$) Code (\$)		Recording each patent assignment per property (times number of properties)		
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over original patent		5 Request for Continued Examination (RCE)		
1205 18 2205 9 ** Reissue claims in excess of 20 and over original patent	1802 900 1802 900	Request for expedited examination of a design application		
	Other fee (specify) Req	for Cert of Correction	100.0d	
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SUBMITTED BY			(Complete (if applicable))
Name (Print/Type)	Mark S. Matkin	Re to ratte in 32,268	Telephone (509) 624-4276
Signature	gent	79 C	Date \$/12/04

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.

: 6,713,346 B2

DATED

: March 30, 2004

INVENTOR(S) : Graham Wolstenholme

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Face Page, (57) Abstract, line 2 -

Replace "forming flash memory. In one implementation, a line of" With --forming FLASH memory. In one implementation, a line of--

Col. 3, line 31 -

Replace "FIG. 1 is a diagrammatic top plan of a semiconductor" With -- FIG. 1 is a diagrammatic top plan view of a semiconductor--

Col. 6, line 10 -

Replace "having portions thereof angled at least 150 from a direction" With -- having portions thereof angled at least 15° from a direction--

Col. 8, line 15 -

Replace "spacer is formed on one of the=source side or the drain side" With -- spacer is formed on one of the source side or the drain side--

Col. 8, line 57, claim 1 -

Replace "A method at forming a line of FLASH memory cells" With -- A method of forming a line of FLASH memory cells—

Col. 10, line 31, claim 10 -

Replace "insulative sidewall spacer having en innermost surface" With -- insulative sidewall spacer having an innermost surface--

> Page 1 of 1

Mailing Address of Sender:

Wells St. John P.S.

601 West First Avenue, Suite 1300

Spokane, WA 99201-3828

Patent No. 6,713,346 B2

UNITED STATES PATENT AND TRADEMARK OFFICE

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